

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No. : 10/599593 Confirmation No.: 1321
Applicant : Dutt et al.
Filing Date : October 2, 2006
Title : Retargetable Instruction set Simulators
Group Art Unit : 2193
Examiner : Wang, Jue S
Docket No. : 703538.4054
Customer No. : 34313

Commissioner for Patents
Mail Stop Amendment
PO Box 1450
Alexandria, VA 22313-1450

Declaration of Mohammad H. Reshadi

I, Mohammad H. Reshadi, declare as follows.

1. I am one of the named joint inventors of the above-identified application.
2. I am also one of the authors, author Mehrdad Reshadi, of the paper entitled, "A Framework for Fast and Retargetable Instruction-Set Architecture", attached hereto as Exhibit 1, and the paper entitled "ReXsim: A Retargetable Framework for Instruction-Set Architecture Simulation," attached hereto as Exhibit 2. Mehrdad is my nickname and I'm known with this name among all my colleagues. Mohammad is my official name.
3. I am a co-author of the papers of Exhibits 1 and 2 with Nikil Dutt, the only other named joint inventor, Prabhat Mishra and Nikhil Bansal. Although Prabhat Mishra and Nikhil Bansal are not inventors of the subject matter described and claimed in the above identified patent application, Prabhat Mishra and Nikhil

Bansal were listed as co-authors on the face of the papers of Exhibits 1 and 2 because they assisted with, among other things, evaluating the techniques described therein, running experiments, and collecting data.

4. In 2003, I was a graduate student at the University of California at Irvine (UCI). As part of the normal procedures at UCI Department of Information and Computer Science, we were afforded the opportunity to publish research papers on the website of the Center for Embedded Computer Systems (CECS) managed by UCI. These papers were assigned Technical Report numbers by the personnel of CECS, who maintained them in an electronic archive and made them available to faculty and students of UCI.

5. In February 2003, I requested a Technical Report number for the paper attached as Exhibit 1 and was given the number "03-05" by CECS. I personally typed the report number "03-05" and the date "February 2003" on the face page of the paper of Exhibit 1. This date signified that I had the paper of Exhibit 1 ready by that date, but the date did not signify that the paper was published to anyone nor was it publicly accessible by any means at that time.

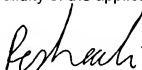
6. I attended the 40th Digital Automation Conference in Anaheim, California, on June 2-6, 2003, as a speaker where I presented a paper entitled "Instruction Set Compiled Simulation: A Technique for Fast and Flexible Instruction Set Simulation". DAC'03, June 2-6, 2003, Anaheim, California, which is attached as Exhibit 3. The presentation was on June 5, 2003. The DAC archive record for this can be found at <http://www.dac.com/DACArchive/pubs/40thadvprg.pdf>

7. I did not submit the paper of Exhibit 1 to CECS in February 2003 to ensure that the first presentation of the subject matter of Exhibit 1 would be at the DAC '03 conference on June 5, 2003.

8. Sometime after the presentation of the paper of Exhibit 3 at the June 2003 DAC Conference, I downloaded the paper of Exhibit 2, which is a final version of the paper of Exhibit 1, to CECS. Sometime after receiving my download of the paper of Exhibit 2, CECS uploaded the paper of Exhibit 2 onto the CECS website making it available for the first time to faculty and students of UCI, who had authorized access at the time to the CECS website. I personally typed the report number "03-05" and the date "February 2003" on the face page of the paper of Exhibit 2. This date signified that I had the paper of Exhibit 2 ready by that date, but the date did not signify that the paper was published to anyone nor was it publicly accessible by any means at that time.

9. The foregoing statements are based on my personal knowledge. All statements herein are made of my own knowledge are true and all statements made on information and belief are believed to be true. I have been warned that willful false statements and the like are punishable by fine or imprisonment, or both (18 U.S.C. 1001) and may jeopardize the validity of the application or any patent issuing thereon.

Date: 12/20/10



Mohammad H. Reshadi